

Practitioner's Docket No. IS9-017

PATENT

CJK
M. Brunson
3/17/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Richard C. Eden and Bruce A. Smetana

Application No.: 09/904,578

Group No.: 2812

Filed: 07/12/2001

Examiner: N. Ngo

For: Power Semiconductor Switching Devices, Power Converters, Integrated Circuit Assemblies, Integrated Circuitry, Power Current Switching Methods, Methods of Forming a Power Semiconductor Switching Device, Power Conversion Methods, Power Semiconductor Switching Device Packaging Methods, and Methods of Forming a Power Transistor

Assistant Commissioner for Patents
Washington, D.C. 20231

REQUEST FOR CORRECTED FILING RECEIPT

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2. There is an error with respect to the following, which is incorrectly entered.

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Natalie King
(type or print name of person certifying)

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(Request for Corrected Filing Receipt)

Error in

Domestic Priority Data as
Claimed by Applicant

Correct data

This application claims benefit of 60/217,860, July 13, 2000.

3. The correction is not due to any error by applicant and no fee is due.

11/19/02
Date



SIGNATURE OF PRACTITIONER

James D. Shaurette

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE RECD	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/904,578	07/12/2001	2812	891	IS9-017	15	66	4

021567
WELLS ST JOHN ROBERTS GREGORY AND MATKIN
SUITE 1300
601 W FIRST AVENUE
SPOKANE, WA 992013828

CONFIRMATION NO. 2358

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OC000000007330928

Date Mailed: 01/18/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Richard C. Eden, Briarcliff, TX;
Bruce A. Smetana, Colton, WA;

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A CONTINUATION OF 60/217,860 07/13/2000

Foreign Applications

If Required, Foreign Filing License Granted 08/27/2001

Projected Publication Date: 05/02/2002

Non-Publication Request: No

Early Publication Request: No

** SMALL ENTITY **

Title

Power semiconductor switching devices, power converters, integrated circuit assemblies, integrated circuitry, power current switching methods, methods of forming a power semiconductor switching device, power conversion methods, power semiconductor switching device packaging methods, and methods of forming a power transistor

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Claims benefit of

Preliminary Class

438

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/904,578
Filing Date July 12, 2001
Inventor Richard C. Eden et al.
Assignee Isothermal Systems Research, Inc.
Group Art Unit 2812
Examiner N. Ngo
Attorney's Docket No. IS9-017
Title : Power Semiconductor Switching Devices, Power Converters, Integrated Circuit
Assemblies, Integrated Circuitry, Power Current Switching Methods, Methods of
Forming a Power Semiconductor Switching Device, Power Conversion Methods,
Power Semiconductor Switching Device Packaging Methods, and Methods of
Forming a Power Transistor

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	Filing Date	July 12, 2001
	First Named Inventor	Richard C. Eden et al.
	Group Art Unit	2812
	Examiner Name	N. Ngo
Total Number of Pages in This Submission	Attorney Docket Number IS9-017	

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